

528

SAMPLE AND HOLD



The **M 528 SAMPLE & HOLD** module is a sample & hold circuit consisting of the following elements:

- The voltage controlled clock oscillator gates the sample circuit and doubles as a VCLFO with triangular and rectangular waveforms.
- External gate input
- Signal input can be the internal noise source (white noise and random voltage, both with separate output jacks) or an external sample signal.
- Dual voltage output jacks with switchable variable glide (portamento) control.